



TC1100 | TC3 I/O

Using TwinCAT I/O, cyclic data can be collected by different fieldbuses in process images. Cyclic tasks drive the corresponding fieldbuses. Various fieldbuses can be operated with different cycle times on one CPU. Applications can directly access the process image. The fieldbuses and the process images are configured in TwinCAT Engineering.

- provides variable-oriented linkage of I/O devices to tasks
- tasks are variable-oriented among each other
- the smallest unit is one bit
- supports both synchronous and asynchronous relationships
- consistent exchange of data areas and process images
- online display in the directory tree
- online watch window
- "Force and Write" for commissioning and for testing task variables and I/O devices
- supported fieldbuses:
 - EtherCAT
 - Lightbus
 - PROFIBUS DP (Master and Slave)
 - Interbus
 - CANopen
 - SERCOS interface
 - DeviceNet
 - Ethernet
 - USB
- SMB (System Management Bus)

Technical data	TC1100
Target system	Windows XP, Windows 7/8/10, Windows CE

Ordering information	
TC1100-0020	TC3 I/O, platform 20 (Economy)
TC1100-0030	TC3 I/O, platform 30 (Economy Plus)
TC1100-0040	TC3 I/O, platform 40 (Performance)
TC1100-0050	TC3 I/O, platform 50 (Performance Plus)
TC1100-0060	TC3 I/O, platform 60 (Mid Performance)
TC1100-0070	TC3 I/O, platform 70 (High Performance)
TC1100-0080	TC3 I/O, platform 80 (Very High Performance)
TC1100-0081	TC3 I/O, platform 81 (Many-core 5...8 cores)
TC1100-0082	TC3 I/O, platform 82 (Many-core 9...16 cores)
TC1100-0083	TC3 I/O, platform 83 (Many-core 17...32 cores)
TC1100-0084	TC3 I/O, platform 84 (Many-core 33...64 cores)
TC1100-0090	TC3 I/O, platform 90 (Other)
TC1100-0091	TC3 I/O, platform 91 (Other 5...8 cores)
TC1100-0092	TC3 I/O, platform 92 (Other 9...16 cores)
TC1100-0093	TC3 I/O, platform 93 (Other 17...32 cores)
TC1100-0094	TC3 I/O, platform 94 (Other 33...64 cores)